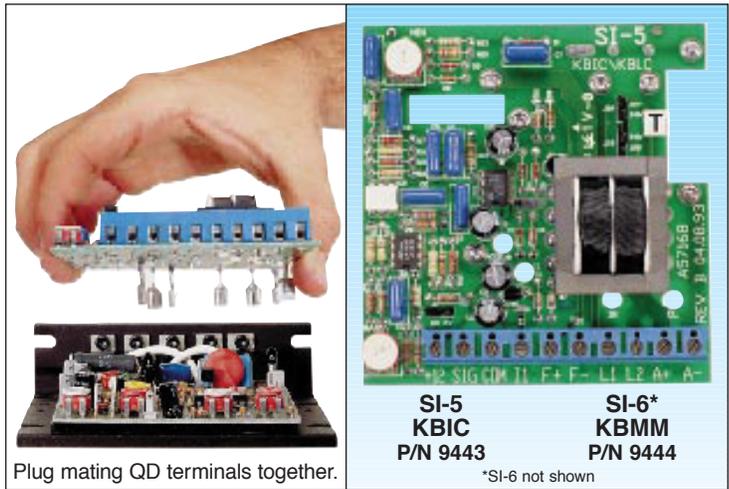


# KBIC<sup>®</sup>, KBMM<sup>™</sup>

## BARRIER TERMINAL BOARD SIGNAL ISOLATORS

MODEL SI-5 for KBIC<sup>®</sup> (P/N 9443)  
MODEL SI-6 for KBMM<sup>™</sup> (P/N 9444)

- Provides Isolation for External Signal and Potentiometer Input
- Operates on 115/230 VAC – 50/60 Hz
- Built-in Trimpots for MIN and MAX
- Includes Barrier Terminal Block



Plug mating QD terminals together.

SI-5  
KBIC  
P/N 9443

SI-6\*  
KBMM  
P/N 9444

\*SI-6 not shown

### SPECIFICATIONS

AC Power Requirements .....	115 or 230 VAC – 50/60 Hz
Signal Input Voltage .....	0 – 5, 0 – 10, 0 – 100 <sup>(1)</sup> , 0 – 200 <sup>(2)</sup>
Signal Input Current .....	4 – 20 mA <sup>(3)</sup>
Maximum Output Voltage .....	11 Volts DC
Maximum Output Current .....	10 mA
Range of MIN Trimpot .....	± 3 Volts
Range of MAX Trimpot ..	0 – 2 Times input Voltage Max. of 11V
Linearity .....	± .1% <sup>(4)</sup>
Temperature Drift .....	4 mV per °C

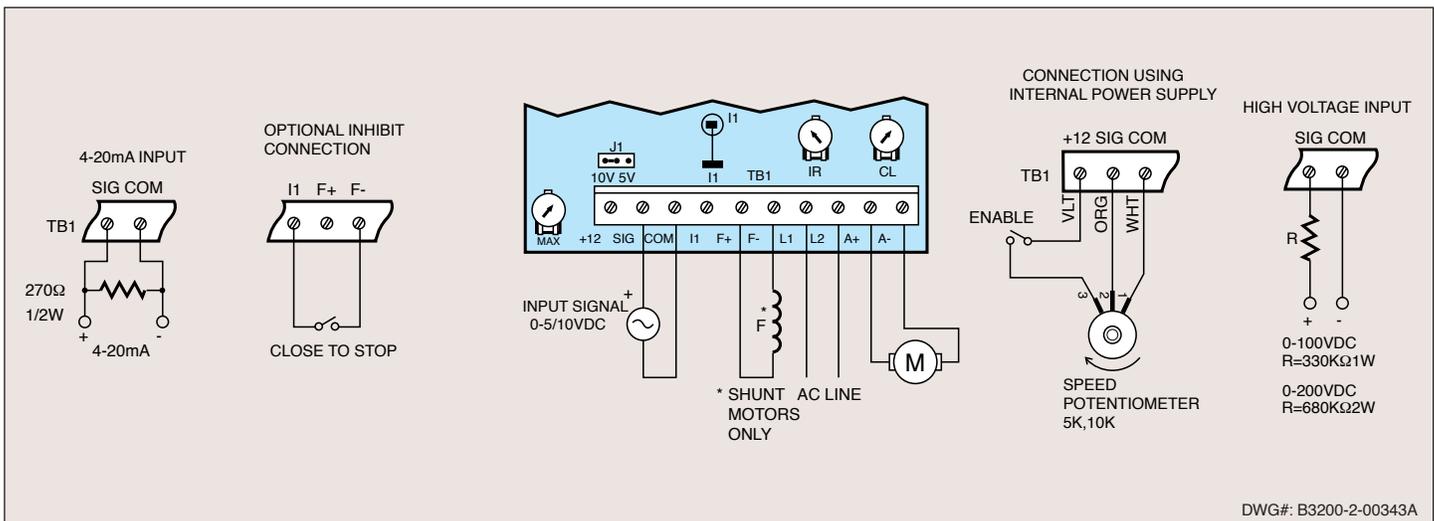
**Notes:**

1. Requires the addition of a 330K – 1W resistor in series with input signal (J1 set to "10V").
2. Requires the addition of a 680K – 2W resistor in series with input signal (J1 set to "10V").
3. Requires the addition of a 270Ω – 1/2W resistor in parallel with input signal (J1 set to "5V").
4. Linearity of SI-5 and SI-6 does not include the linearity specification of the speed control.

### DESCRIPTION

The SI-5 and SI-6 Barrier Terminal Board Signal Isolators convert standard KBIC<sup>®</sup> and KBMM<sup>™</sup> controls to an isolated input. The Isolators contain a selectable jumper (J1) that allows for either a 0 – 5 or 0 – 10 VDC input signal. By using external resistors, the input signal can be changed to 0 – 100 VDC, 0 – 200 VDC and 4 – 20 mA. The output voltage is 0 – 10 VDC which can be rescaled via the built-in MIN and MAX trimpots. Selectable AC Line jumpers (J2A, J2B) allow the SI-5 and SI-6 to be used either with 115 or 230 VAC controls. In addition, a +12 VDC power supply voltage is furnished which can be used to power remote transducers or provide an isolated speed potentiometer. Installation is made by simply mating the SI-5 with the KBIC<sup>®</sup> and the SI-6 with the KBMM<sup>™</sup> speed controls via the built-in quick-connect terminals.

### CONNECTION DIAGRAMS



DWG#: B3200-2-00343A

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